Chunghwa Picture Tubes, Ltd.
Technical Specification

To : **YIH HSING ENTERPRISE CO.,LTD.**
Date : 2006/08/23

CPT TFT-LCD

CLAA150XP01Q Y

ACCEPTED BY :

APPROVED BY	CHECKED BY	PREPARED BY
	溫文良	Product Planning Management General Division

Prepared by : TFT-LCD Product Planning Management Division

CHUNGHWA PICTUER TUBES, LTD.
1127 Hopin Rd., Padeh, Taoyuan, Taiwan 334, R.O.C.
TEL: +886-3-3675151 FAX: +886-3-377-3001

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RECORD OF REVISIONS

Revision No.	Date	Page	Description
Tentative v.1	2006-07-06	--	First issued.
Final v.1	2006-08-22		Final

CONTENTS

1. OVERVIEW

CLAA150XP01Q is 15”(38.1cm) color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, LVDS driver ICs, control circuit and backlight(CCFL, 2 tubes). By applying 8 bit digital data, 1024×768, drived by 3.3 voltages, 16.2M-color images are displayed on the 15” diagonal screen. The module structure is fixed by iron frame, without the inverter for the backlight. Interface of data and control signals is typ.General specification are summarized in the following table:

ITEM	SPECIFICATION
Display Area (mm)	304.1(H)x228.1(V) (15.0-inch diagonal)
Number of Pixels	1024x3(H) x 768(V)
Pixel Pitch (mm)	0.297(H) x 0.297(V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally White, TN
Number of Colors	16.2M (6bits+FRC)
Optimum Viewing Angle	6 o’clock
Brightness (cd/m^2)	250cd/m^2 (Typ.)(center, 8.0mA)
Viewing Angle	70/-70/65/-60 (Typ.)
Response time	8ms
Surface Treatment, Haze	Anti-glare
Color Saturation	65%
Module Size (mm)	326.5(W) x 253.5(H) x 11.0(D) (Typ.)
Power consumption	11.7 Watt (Typ)
Module Weight (g)	1070 (Typ.)
Backlight Unit	CCFL, 2 tubes (top x 1/bottom x1), Edge light

The LCD Products listed on this document are not suitable for use of aerospace equipment, submarine cables, and nuclear reactor control system and life support systems. If customers intend to use these LCD products for above application or not listed in "Standard" as follows, please contact our sales in advance.

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tool, Industrial robot, Audio and Visual equipment, Other consumer products.

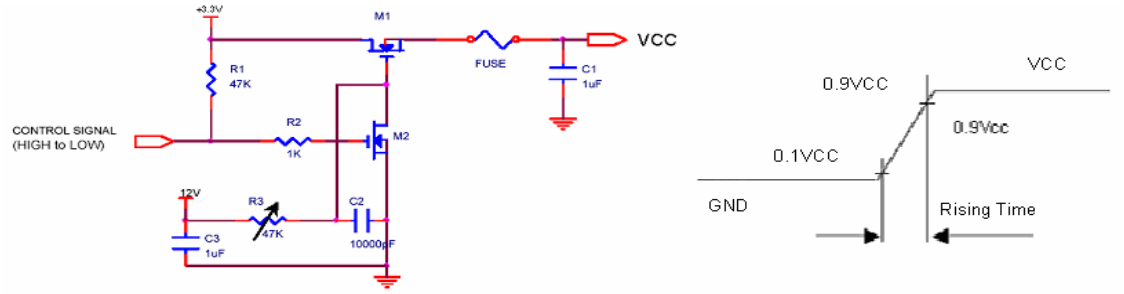
2. ABSOLUTE MAXIMUM RATINGS

The following are max. values which, if exceeded, may cause faulty operation or damage to the unit.

ITEM	SYMBOL	MIN.	MAX.	UNIT	Remark
Power Supply Voltage For LCD	VCC	--	4.0	V	--
IDDD Rush Current	IRUSHd	--	4.0	A	*1)
Lamp Voltage	VL	531	786	Vrms	*2)
Lamp Current	IL	3	8.5	mArms	*2)
Lamp Frequency	FL	40	80	kHz	*2) *3)
Operation Temperature (Surrounding)	Top	0	50	℃	*4). 5). 6). 7) 9)
Storage Temperature	Tstg	-20	60	℃	*4). 5). 6) 9)
Delayed Discharge Time	TD	--	1	sec	*8)

*1) Inrush Measurement Condition

The rising time of VCC is 550 μsec (measured conditions are described below).



*2) These are properties of single lamp(without backlight).

a. Lamp life-time relate to the lamp current, please operate following statement Back light system at page 7.

b. When lamp current over the definition of absolute max. value , life-time of the product will decay rapidly or operate unusual.

*3) The frequency range will not affect to lamp life and reliability characteristics.

a. Electrical and optical characteristics will display well at 40~60 kHz frequency.

b. It would not damage the lifetime and reliability of the panel at 40~80 kHz frequency.

*4) The relative temperature and humidity range are as below sketch, 90%RHMax. (Ta≦40℃)

*5) The maximum wet bulb temperature ≦39℃(Ta＞40℃) and without dewing.

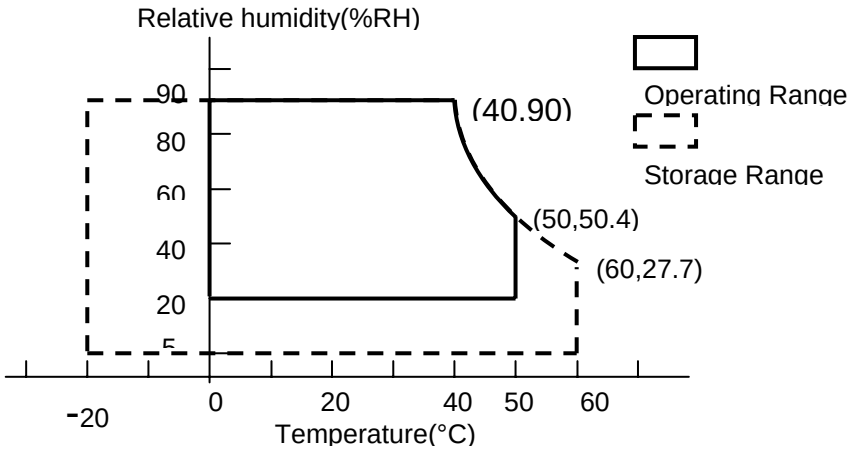
*6) If you use the product in a environment which over the definition of temperature and humidity, it will concern for visual quality.

*7) If you operated the product in normal temperature range, the center surface of panel should be under 60℃.

*8) Delay lighting testing needs the volt above start voltage Vrms. Before the procedure tube needs typical lighting for 1 min. and stay in the temperature 25±2 ℃ for 24 hours and then testing in the same condition in dark room.

*9)Humidity≦85%RH without condensation. Relative Humidity ≦90% (Ta≦ 40℃)

Wet Bulb Temperature ≦39℃(Ta≧40℃)



3. ELECTRICAL CHARACTERISTICS

(1) TFT-LCD Ta=25℃

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
Power Supply Voltage for LCD	VCC	3.0	3.3	3.6	V	*1)
Power Supply Current for LCD	ICC	--	450	600	mA	*2)
Permissive Ripple Voltage for Logic	VRPd	--	--	100	mVp-p	Vin=+3.3V
Differential Resistance	Zm	70	100	110	Ω	*3)

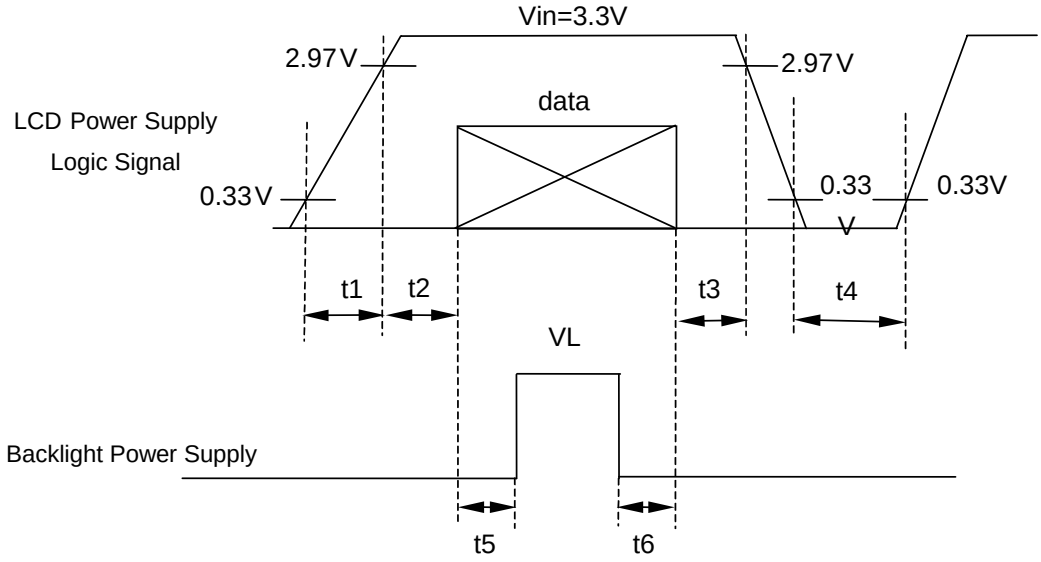
[Note]

*1)Power、data sequence

0.5 ms＜t1≦10ms 500 ms≦t4

0 ms＜t2≦50 ms 200 ms≦t5

0 ms＜t3≦50 ms 200 ms≦t6

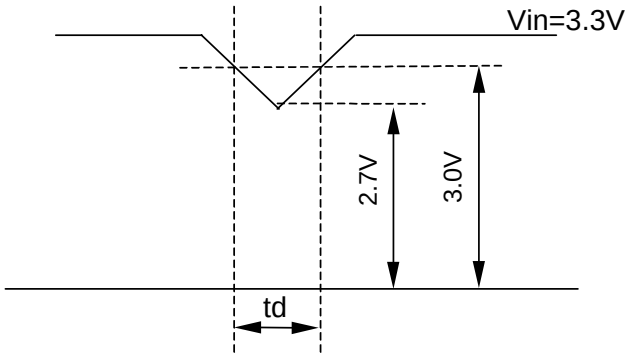


Data: RGB DATA, DCLK

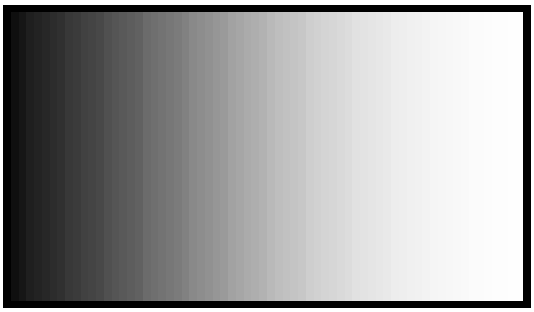
VCC-dip State：

1) When 2.7 V≦VCC＜3.0V，td≦10 ms

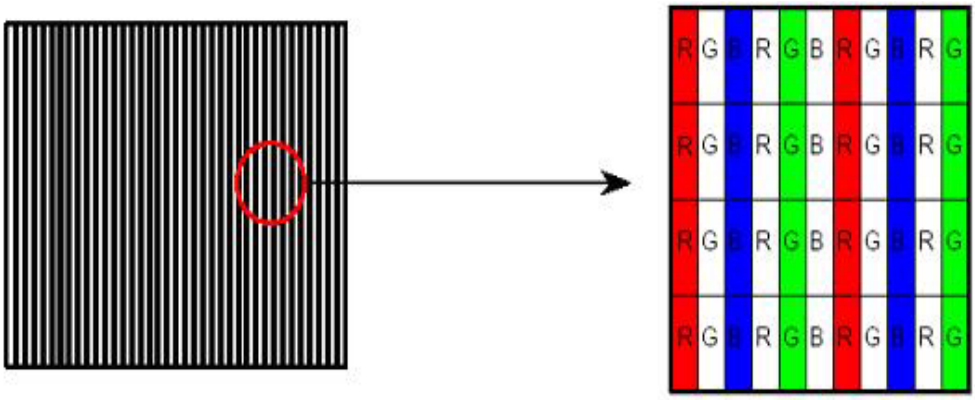
2) When VCC＜2.7V, it will reset the power. VCC dip conditions should follow VCC turn on conditions.



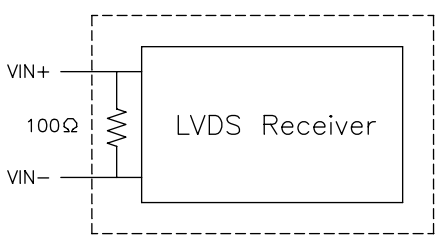
*2) 1. Typical value is measured when displaying horizontal gray scale line pattern:
0~255 gray level, 768 line mode
VCC= +3.3 V，f_{CLK}=65 MHz

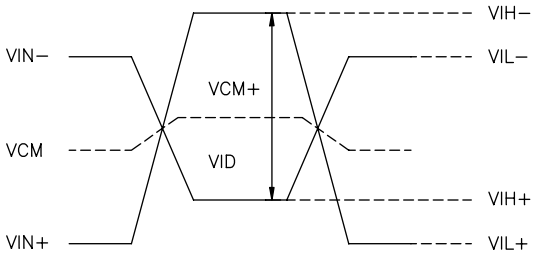


2. Max. value:
768 line mode，VCC= +3.3 V，f_{CLK}=65 MHz



*3) LVDS Signal definition :





VID = VIN_+ – VIN-,
△VCM = | VCM_+–VCM- |,
△VID = | VID_+–VID- |,
VID+ = | VIH_+–VIH- |,
VID- = | VIL_+–VIL- |,
VCM =(VIN_+ +VIN-)/2,
VCM+ =(VIH_+ +VIH-)/2,
VCM- =(VIL_+ +VIL-)/2

VIN+ ：Positive differential DATA & CLK Input
VIN- ：Negative differential DATA & CLK Input

(2) Backlight *1)

(a) Electrical Characteristics

Ta=25℃

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
Lamp Voltage	VL	540	600	660	Vrms	*2) ; IL=8.0mA
Lamp Operation range	IL	3.0	8.0	8.5	mArms	*1)
Lamp Standard Current	IL	7.5	8.0	8.5	mArms	*2)
Inverter Frequency	FI	40	—	60	kHz	*2) *4) *5)
Start up Lamp Voltage	VS	—	—	1050	Vrms	Ta=25℃ *2) *6)
		—	—	1350	Vrms	Ta=0℃ *2) *6)

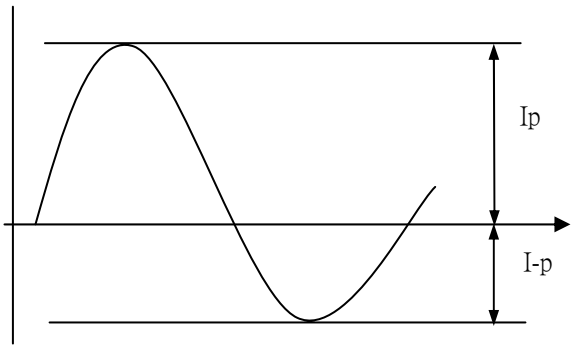
(b) Lamp Life

ITEM	IL @3.0 mA	IL @7.5 mA	IL @8.0 mA	IL @8.5 mA	UNIT	REMARK
Lamp Life Time	Min. 50,000	Min. 35,000	Min. 30,000 Typ. 40,000	Min. 20,000	hr	Continuous Operation*7)
Turn-on and Turn-off Operation	--	--	Min.100,000	--	time	Continuous Operation*8)

[Note]

*1) If the waveform of light up-driving is asymmetric, the distribution of mercury inside the lamp tube will become unequally or will deplete the Ar gas in it. Then it may cause the abnormal phenomenon of lighting-up. Therefore, designers have to try their best to fulfill the conditions under the inverter designing-stage as below:

- The degrees of unbalance： ＜10%



Ip：high side peak

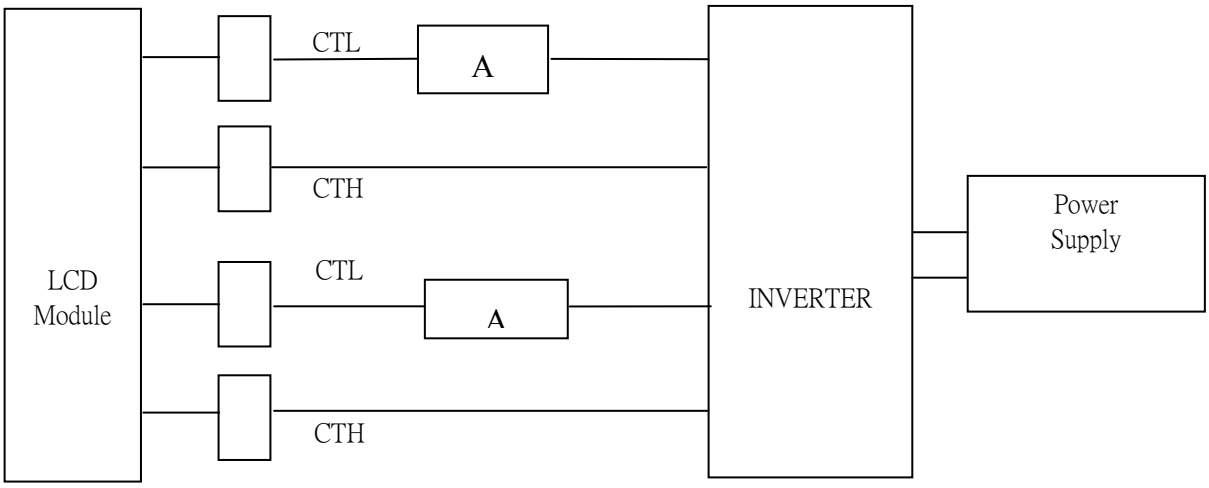
I-p：low side peak

A：The degrees of unbalance = | Ip – I-p | / Irms ×100 (%)

B：The ratio of wave height = Ip (or I-p) / Irms

- The ratio of wave height： ＜√2 ±10%

*2) These are properties of single lamp(without backlight).
*3) Lamp Current measurement method (The current meter is inserted in cold line)



*4) The frequency range can be kept within +/- 10% range of electrical and optical characteristics but not include color coordinate.
*5) Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, please adjust lamp frequency, and keep inverter as far from module as possible or use electronic shielding between inverter and module to avoid the interference
*6) The maximum value of starting lamp voltage is defined as the probably biggest value of starting lamp voltage, hence the design of starting lamp voltage for inverter must be equal to or higher than maximum starting lamp voltage.
*7) Definition of the lamp life time：
Luminance reduced to 50% of initial value.
When lamp current over 8.5mA, lamp life time will drop rapidly. If over 8.5mA, it will come up safety issue. But if it lower than 3.0mA, the lamp will be damaged.
*8) The condition of Turn-on and Turn-off operation is as below:
a. Lamp current is 8.0mA. Ta=25±5 ℃.
b. Frequency is 10 sec.(on)/10 sec.(off)
c. Repeat it for 100 thousand times
d. The lamp life time still match the definition of lamp life time. See the Note *7).

4. Connector Interface PIN & Function

(1) CN1

Outlet connector: MSB240420 (STM) or equivalent

Plug connector: DF14-20S-1.25C (Hirose) or equivalent

PIN #	SYMBOL	FUNCTION
1	VCC	+3.3V Power Supply
2	VCC	+3.3V Power Supply
3	GND	GND
4	GND	GND
5	RXIN0-	Negative LVDS Differential Data Input
6	RXIN0+	Positive LVDS Differential Data Input
7	GND	GND
8	RXIN1-	Negative LVDS Differential Data Input
9	RXIN1+	Positive LVDS Differential Data Input
10	GND	GND
11	RXIN2-	Negative LVDS Differential Data Input
12	RXIN2+	Positive LVDS Differential Data Input
13	GND	GND
14	RXCLK	Negative LVDS Differential Clock Input
15	RXCLK	Positive LVDS Differential Clock Input
16	GND	GND
17	RXIN3-	Negative LVDS Differential Data Input
18	RXIN3+	Positive LVDS Differential Data Input
19	GND	GND
20	NC	Reserved

1) Please keep the NC Pin and don't connect it to GND or other signals.

2) GND Pin must connect to the ground, don't let it be a vacant pin.

(2) CN2, 3 (BACKLIGHT)

Backlight-side connector: BHR-03VS-1 (JST)

Inverter-side connector: SM03B-BHS-1 (JST)

CN2

PIN #	SYMBOL	FUNCTION
1	CTH1	High Voltage
2	--	Empty
3	CTL1	Low Voltage

CN3

PIN #	SYMBOL	FUNCTION
1	CTH2	High Voltage
2	--	Empty
3	CTL2	Low Voltage

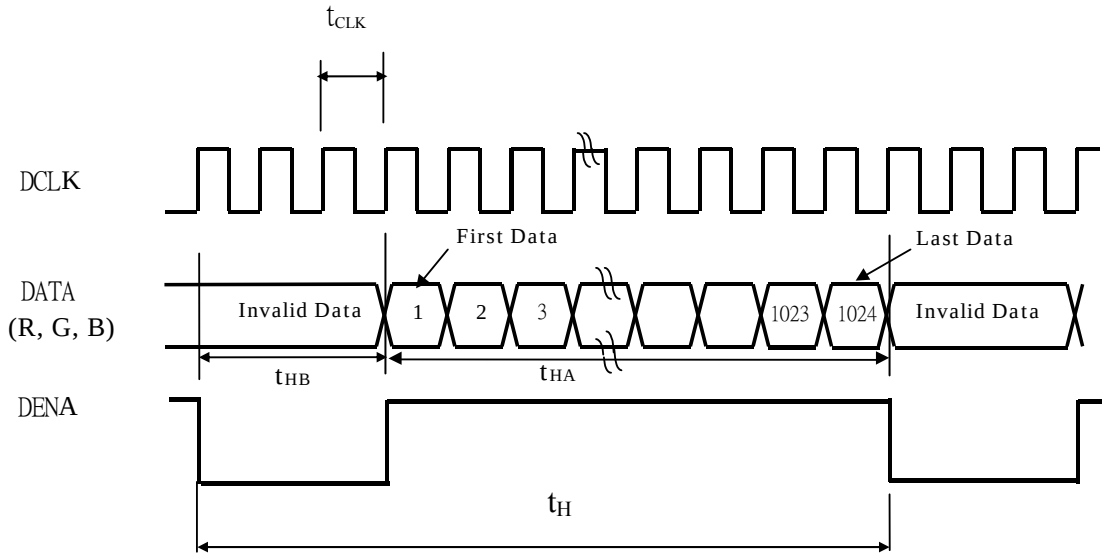
5. INTERFACE TIMING

(1) Timing Characteristic

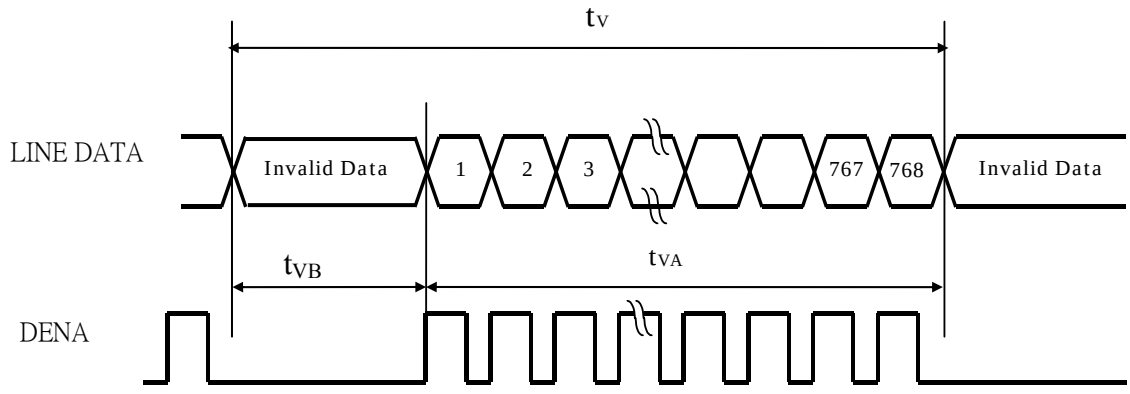
ITEM				SYMBOL	MIN.	TYP.	MAX.	UNIT
LCD Timing	DCLK		Frequency	f_{CLK}	50	65	80	MHz
			Cycle	t_{CLK}	12.5	15.3	20	ns
	DENA	Horizontal	Vertical line rate	f_H	43.48	48.36	58.39	kHz
			Horizontal total time	t_H	1150	1344	1370	t_{CLK}
			Horiaontal effective time	t_{HA}	--	1024	--	t_{CLK}
			Horizontal blank time	t_{HB}	126	320	346	t_{CLK}
		Vertical	Vertical frame Rate	Fr	55	60	75	Hz
			Vertical total time	t_V	794	806	860	t_H
			Vertical effective time	t_{VA}	--	768	--	t_H
			Vertical blank time	t_{VB}	26	38	92	t_H

(2) Timing Chart

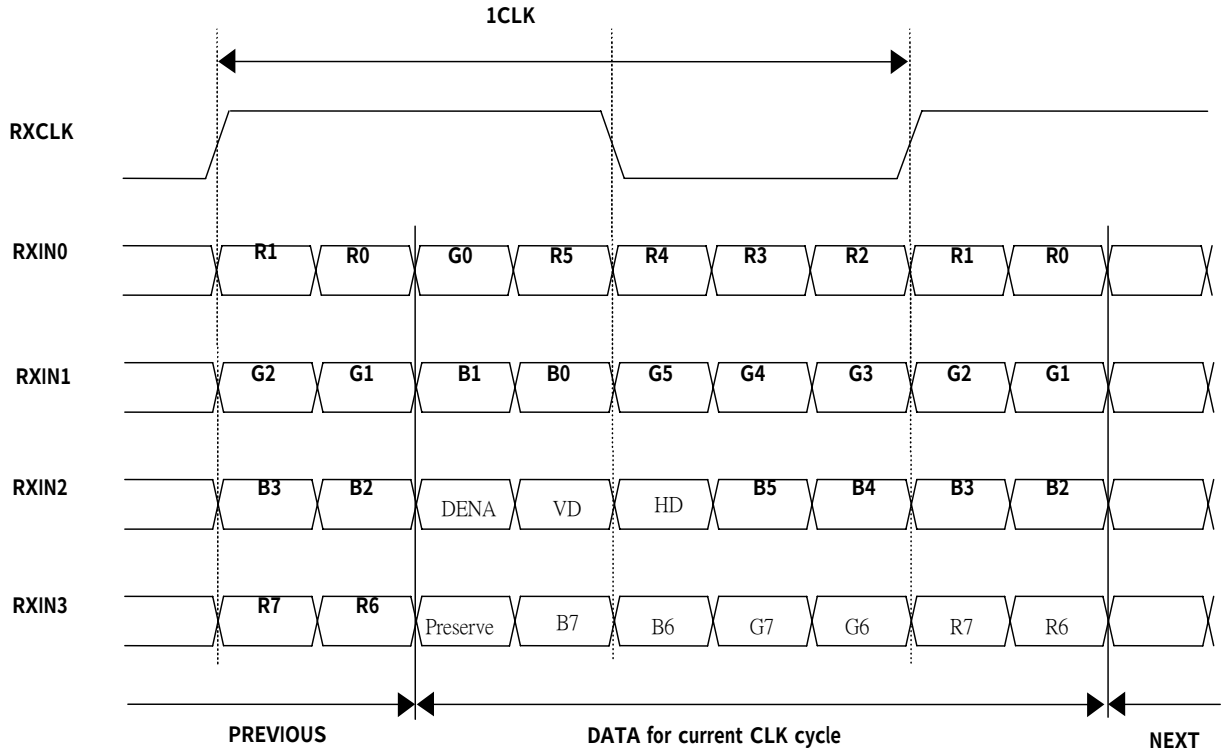
a. Horizontal signal:



b. Vertical signal:



(3) Data Mapping



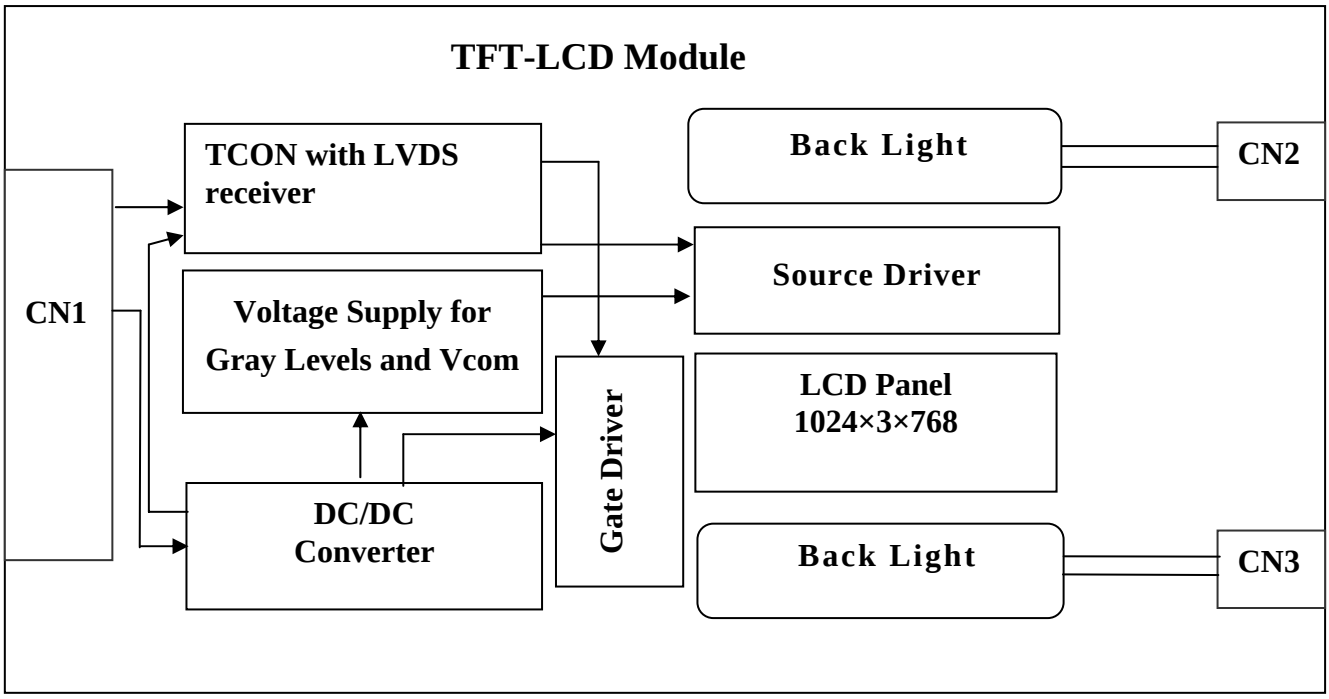
(4) Color Data Assignment

Color	Input Data	MSB							LSB	MSB							LSB	MSB							LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	Green (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Blue (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Blue (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

[Note]

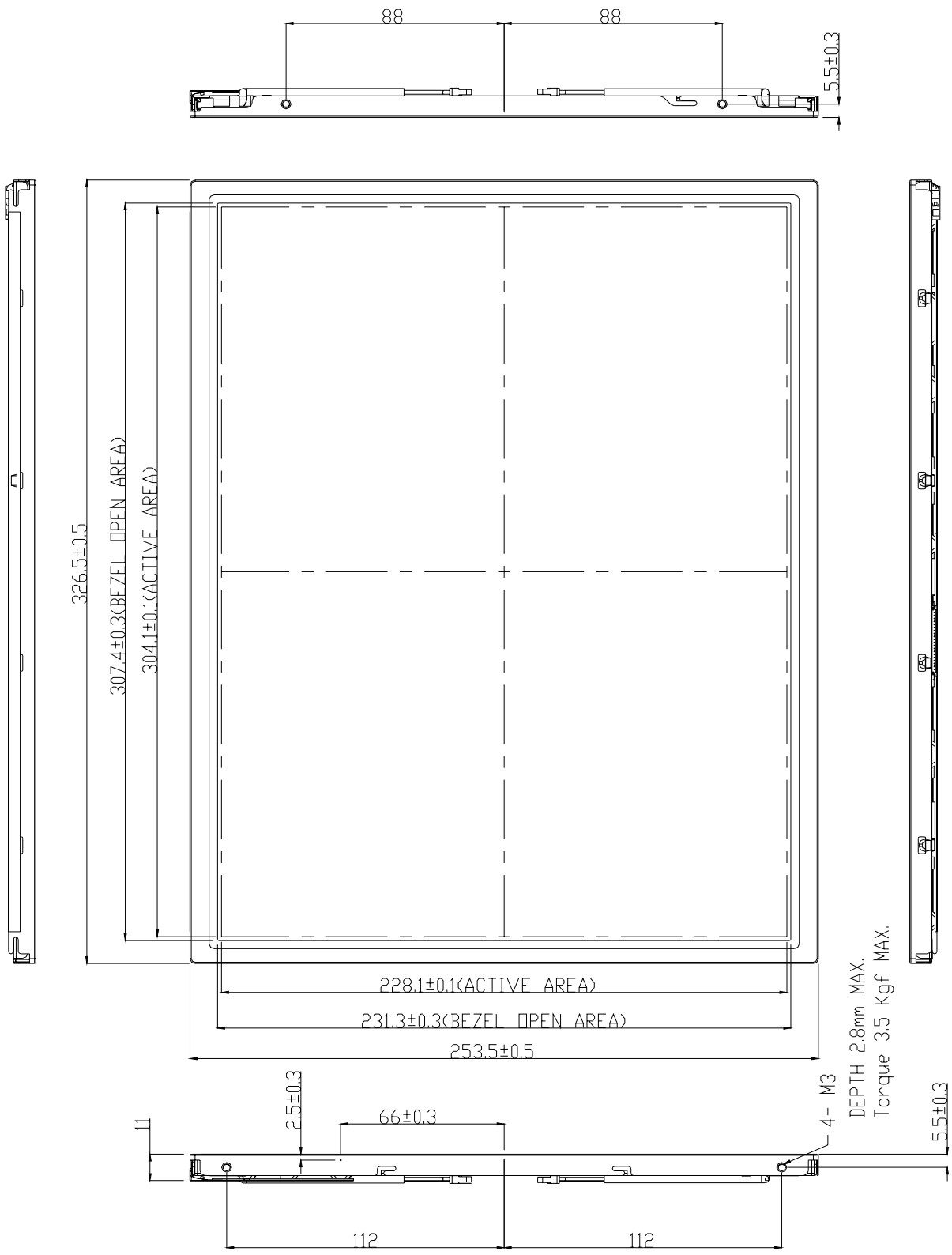
1) Definition of gray scale:
   Color (n): n indicates gray scale level; larger n means brighter level.
2) Data: 1-High, 0-Low.

6. BLOCK DIAGRAM

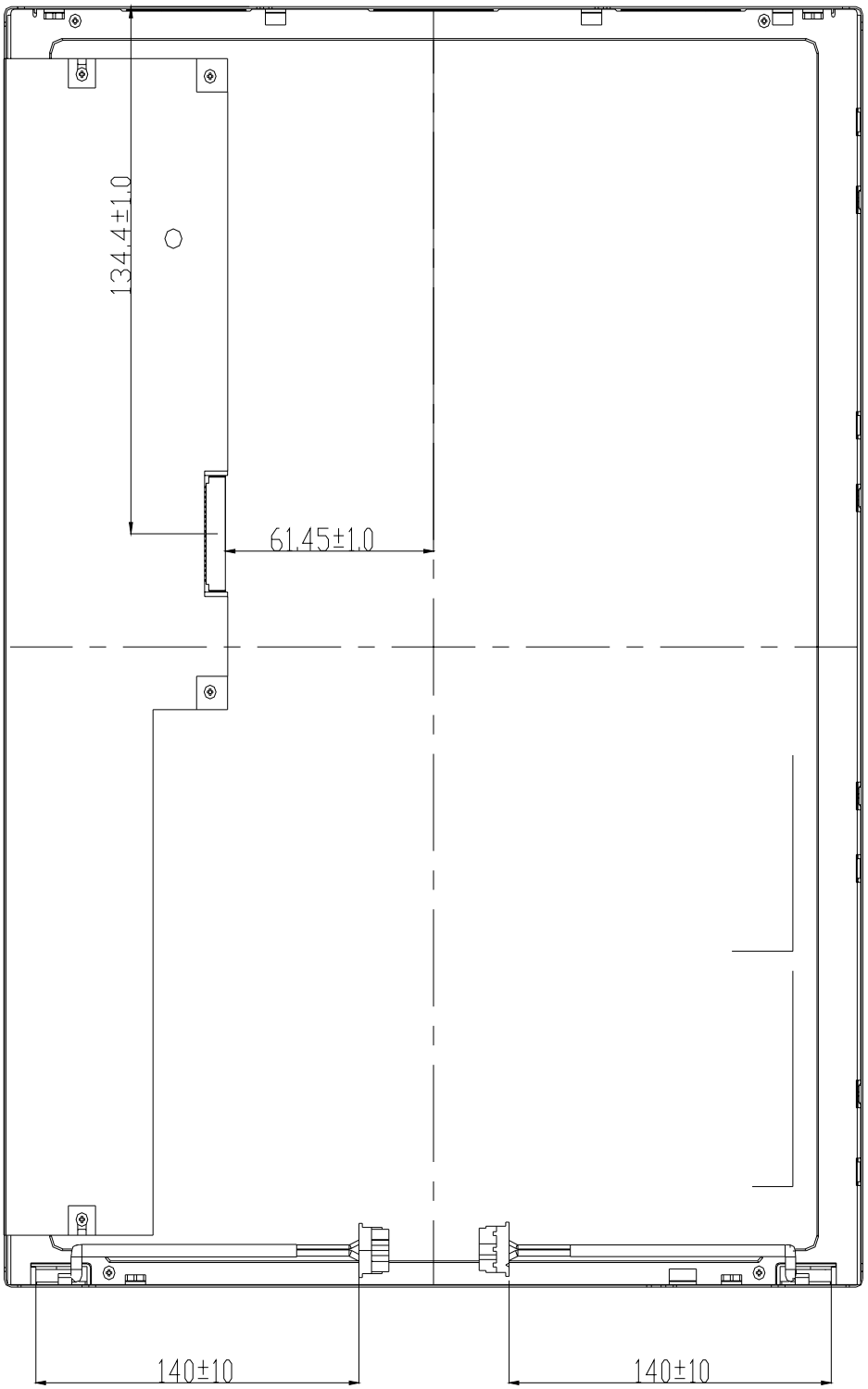


7. MECHANICAL SPECIFICATION

(1) Front side (Tolerance is ± 0.5mm unless noted) [Unit: mm]



(2) Rear side (Tolerance is ±0.5mm unless noted) [Unit: mm]



8. OPTICAL CHARACTERISTICS

Ta = 25°C, VCC=3.3V

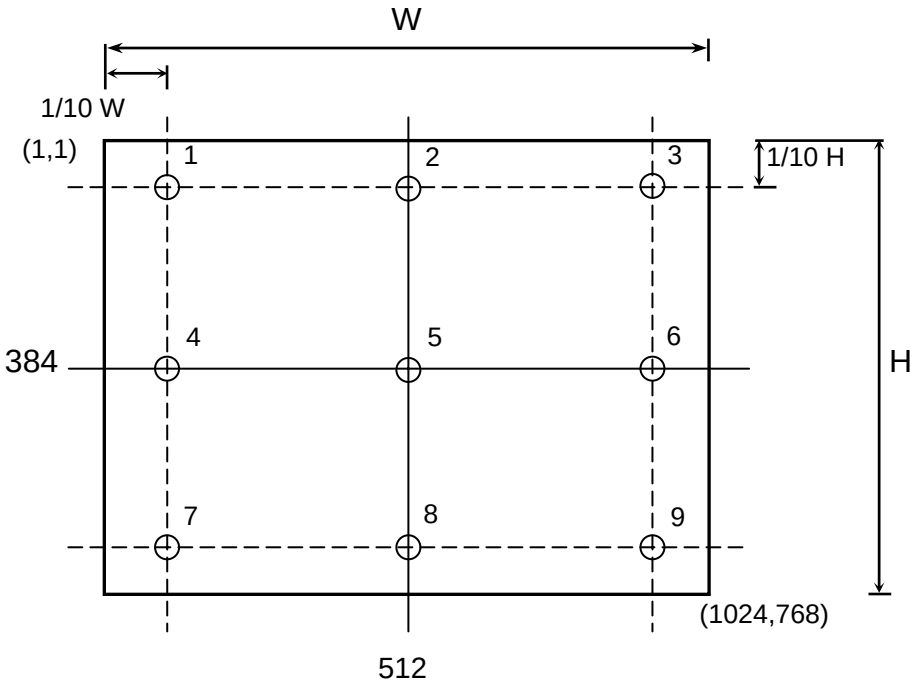
ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Contrast (CEN)		CR	θ= ϕ= 0°	450	500	--	--	*1)
Luminance (CEN)		L	θ= ϕ= 0°	180	250	--	cd/m^2	*2)
9P Luminance (AVG.)		L	θ= ϕ= 0°	160	225	--	cd/m^2	*2)
9P Uniformity (9P)		ΔL	θ= ϕ= 0°	75	80	--	%	*2)
Response Time		Tr	θ= ϕ= 0°	--	2	4	ms	*4)
		Tf	θ= ϕ= 0°	--	6	10	ms	
Image sticking		Tis	4 hours	--	--	12	s	*5)
Cross talk		CMR	θ=ψ=0°	--	--	1	%	*6)
View angle	Horizontal	ϕ	CR≧5	**-75~75**	**-85~85**	--		***3)**
	Vertical	θ		**-75~75**	**-85~85**	--		
	Horizontal	ϕ	CR≧10	**-60~60**	**-70~70**	--		***3)**
	Vertical	θ		**-50~55**	**-60~65**	--		
Color Coordinates	White	x y	θ= ϕ= 0°	0.283 0.299	0.313 0.329	0.343 0.359	--	
	Red	x y		0.613 0.305	0.643 0.335	0.673 0.365		
	Green	x y		0.270 0.555	0.300 0.585	0.330 0.615		
	Blue	x y		0.113 0.049	0.143 0.079	0.173 0.109		
Gamma		γ	VESA	2.0	2.2	2.4	--	*7)

[Note]

These items are measured using BM-5A (TOPCON) under the dark room condition (no ambient light).
Measurement Condition: IL=8.0 ± 0.1mA
Inverter: ConverTek, model: CVT010330BI31402
Definition of these measurement items is as follows:

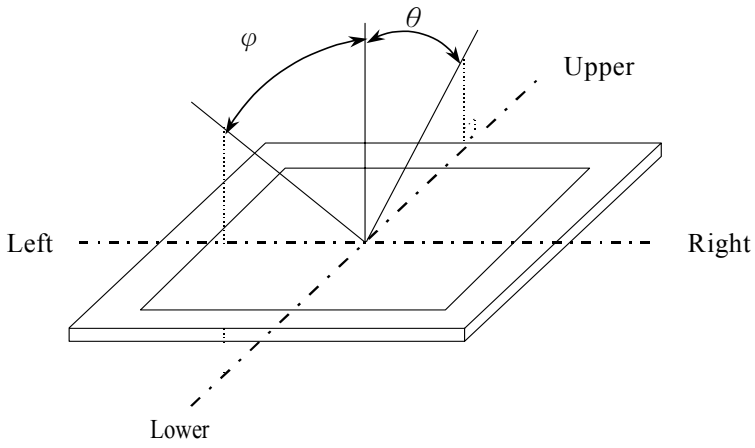
*1) Definition of Contrast Ratio
CR=ON (White) Luminance/OFF (Black) Luminance

*2) Definition of Luminance and Luminance uniformity
Central luminance: The white luminance is measured at the center position “5” on the screen, see Fig.1 below.
9P Luminance (AVG): The white luminance is measured at measuring points 1 to 9, and take the average value, see the following picture.
9P Uniformity: Δ L = (L_{MIN} /L_{MAX}) ×100%



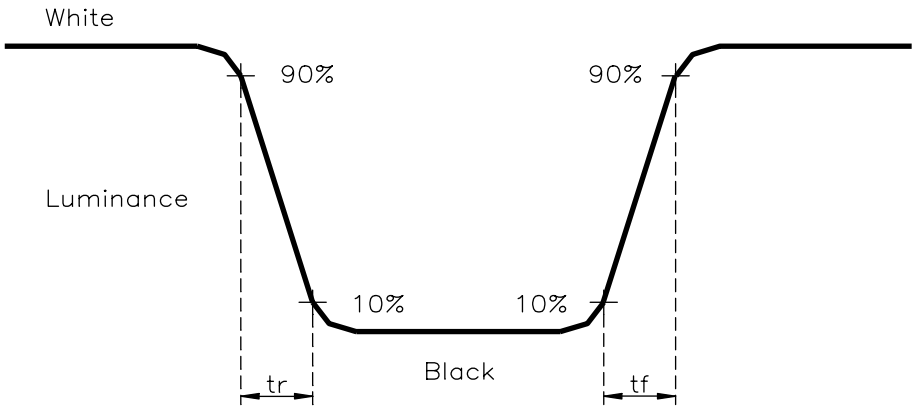
Measurement Position

*3) Definition of Viewing Angle(θ , ϕ)



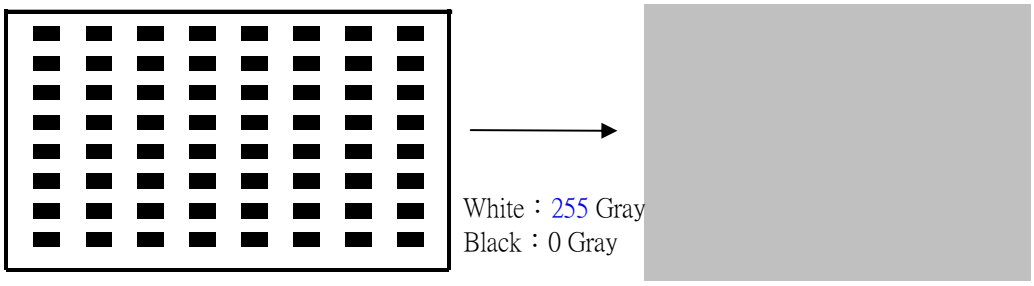
*4) Definition of Response Time

Change the module frame to Black/white pattern and use Westar TDR-100 to measure tr and tf under 25℃ room temperature.



*5) Definition of image sticking

Continuously display the test pattern showing in the below figure for 4hrs at 25℃, then switch to gray pattern (the 120nd gray level pattern).



Judgment standard: Image doesn't disappear in 12 seconds, but it looks like Mura Rank 2 as standard sample.

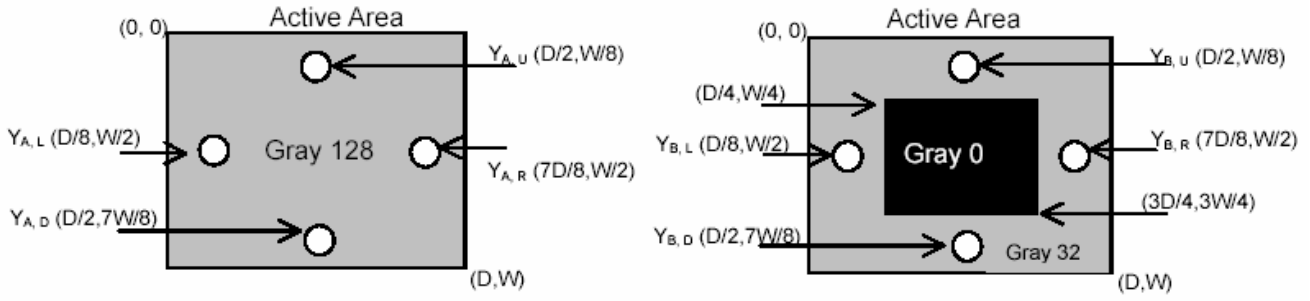
*6) Cross talk Modulation Ratio:

$CT = |Y_B - Y_A| / Y_{Ax} \times 100\%$

Y_A、Y_B measure position and definition

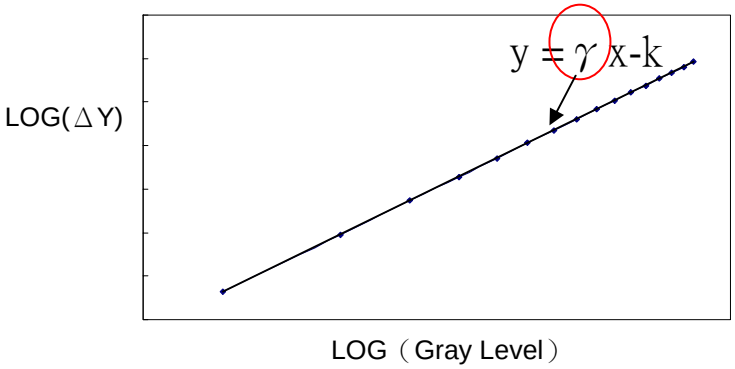
Y_A means luminance at gray level 128(exclude gray level 0 pattern)

Y_B means luminance at gray level 128(include gray level 0 pattern)



*7) Defination Gamma(VESA)

Based on Customer Sample, take the average value as a standard center value and the variation range of Gamma value caused by loop voltage error should be between +/- 0.2. the bellow figure shows how to obtain the gamma curve and γ (from gray level: 0、16、32-----224、240、255).



9. RELIABILITY TEST CONDITIONS

(1) Temperature 、Pressure and Humidity

TEST ITEMS	CONDITIONS
High Temperature High Humidity Operation	50℃; 90%RH; 240hrs (No condensation)
High Temperature High Humidity Storage	60℃; 90%RH; 48hrs (No condensation)
Temperature & Pressure Operation	0℃; 260hpa; 24hrs
Temperature & Pressure Storage	-30℃; 260hpa; 24hrs
High Temperature Operation	50℃; 240hrs
High Temperature Storage	60℃; 240hrs
Low Temperature Operation	0℃; 240hrs
Low Temperature Storage	-20℃; 240hrs
Thermal Shock	Between -20℃,1hr ~ 60℃,1hr ; 100 Cycles

(2) Shock & Vibration

ITEMS	CONDITIONS
Shock (Non-Operation)	Shock level: 1470 m/s^2 (150 G) Waveform: half sinusoidal wave, 2 ms Number of shocks: one shock input in each direction of three mutually perpendicular axes for a total of six shock inputs
Vibration (Non-Operation)	Vibration level: 10.78 m/s^2 (1.25 G) Waveform: sinusoidal wave Frequency range: 5 to 500 Hz Frequency sweep rate: 0.5 octave/min Duration: one sweep from 5 to 500Hz in each of three mutually perpendicular axis(each x, y, z axis: 1 hour, total 3 hours)
	Vibration level: 11.27m/s^2(1.15G) zero to peak Waveform: random Frequency range: 5 to 200 Hz Duration: one sweep from 5 to 200Hz in each of three mutually perpendicular axis(each x,y,z axis: 1 hour) **It is testing with package.**

(3) ESD testing

Test Item	Test statements
Connector	200 pF, 0 Ω, ±250 V By using contact-mode to discharge each pin one time (every 1sec) and then check the module frame.
Module	1. Test statements:150 pF, 330 Ω, ±15kV Under non-operation testing conditions, by using air-mode to discharge each test point 25 times (discharge time space:1s) continueously and then check the module frame. 2. Test statements:150pF, 330Ω, ±2KV Under operation testing conditions, by using contact mode to discharge the front bezel and using air mode to discharge the points of panel.

(4) Judgment standard

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect.
Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

10. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling- TFT-LCD products.

10.1 **ASSEMBLY** PRECAUTION

(1) Please use the mounting hole on the module side in installing and do not beading or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.

(2) Please design display housing in accordance with the following guidelines.

- Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
- Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0 mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
- Design the inverter location and connector position carefully so as not to give stress to lamp cable, or not to interface the LCD module by the lamp cable.
- Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interface the LCD module. Approximately 1.0mm of the clearance in the design is recommended.

(3) Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)

(4) Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.

(5) Please wipe out LCD panel surface with absorbent cotton or soft clothe in case of it being soiled.

(6) Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.

(7) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

(8) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.

(9) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.

10.2 OPERATING PRECAUTIONS

(1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.

(2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.

(3) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.

(4) A condensation might happen on the surface and inside of LCD module in case of sudden change of ambient temperature.

(5) Please pay attention to displaying the same pattern for very long time. Image might stick on LCD. If then, time going on can make LCD work well.

(6) Please obey the same caution descriptions as ones that need to pay attention to ordinary electronic parts.

10.3 PRECAUTIONS WITH ELECTROSTATICS

(1) This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.

(2) Please remove protection film very slowly on the surface of LCD module to prevent from electrostatics occurrence.

10.4 STORAGE PRECAUTIONS

(1) When you store LCDs for a long time, it is recommended to keep the temperature between 0℃ ~40℃ without the exposure of sunlight and to keep the humidity less than 90%RH.

(2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60℃ 90%RH.

(3) Please do not leave the LCDs in the environment of low temperature; below -20℃.

10.5 SAFETY PRECAUTIONS

(1) When you waste LCDs, it is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leaks out of a damaged-glass cell and comes in contact with the hands, wash off throughly with soap and water.

10.6 OTHERS

(1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays.

(2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.

(3) For the packaging box, please pay attention to the followings:

- Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
- Please do not pile them up more than 3 boxes. (They are not designed so.) And please do not turn over.
- Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
- Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)

11. PACKING FORM

- Package quantity in one carton: 10 pieces.
- Carton size:485(L)×365(W)×365(H) (unit：mm)
- For domestic transportation only.

12. SAFETY

We will try our best comply the directive 2002/95/EC of the European, and that we will do our possible not to use or use exceeding the limits of banned substances. We also comply with product-related environmental laws and regulations in manufacturing process and do our best to achieve global environmental protection standards.