(V) Preliminary Specifications
( ) Final Specifications

Module	8.4 Inch Color TFT-LCD
Model Name	G084SN05 V7

Customer	**Date**	**Approved by**	**Date**
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Note: This Specification is subject to change without notice.		General Display Business Division / AU Optronics corporation	

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Record of Revision

Version and Date	Page	Old description	New Description
0.0 2008/1/3	All	First Edition	
0.1 2008/1/14	6	Viewing Angle 70/70/65/55(R/L/U/D)	Viewing Angle 75/75/75/75(R/L/U/D)
	13	Lamp Life Typ. 50,000hrs	Lamp Life Min. 50,000hrs VCFL:430 v PCFL:5.16 w
	22		7.1.1 Connector Illustration

1. Operating Precautions

1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
2) Be sure to turn off power supply when inserting or disconnecting from input connector.
3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
7) Do not open nor modify the module assembly.
8) Do not press the reflector sheet at the back of the module to any direction.
9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the CCFL Reflector edge. Instead, press at the far ends of the CFL Reflector edge softly. Otherwise the TFT Module may be damaged.
10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
11) After installation of the TFT Module into an enclosure (Notebook PC Bezel, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
12) Cold cathode fluorescent lamp in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
14) The LCD module is designed so that the CFL in it is supplied by Limited Current Circuit (IEC60950 or UL1950). Do not connect the CFL in Hazardous Voltage Circuit.
15) Severe temperature condition may result in different luminance, response time and lamp ignition voltage.
16) Continuous operating TFT-LCD display under low temperature environment may accelerate lamp exhaustion and reduce luminance dramatically.
17) The data on this specification sheet is applicable when LCD module is placed in landscape position.
18) Continuous displaying fixed pattern may induce image sticking. It's recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.

2. General Description

This specification applies to the 8.4 inch color TFT LCD module G084SN05 V.7.

This module is designed for display units for Industrial Applications.

The screen format is intended to support the SVGA (800(H) x 600(V)) screen and 16.2M (RGB 8-bits) or 262k colors (RGB 6-bits).

All input signals are LVDS interface compatible.

The module does not contain an inverter card for backlight.

This is an RoHS product.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 ℃ condition:

Items	Unit	Specifications
Screen Diagonal	[inch]	8.4 (213.4mm)
Active Area	[mm]	170.4(H) x 127.8(V)
Pixels H x V		800x3(RGB) x 600
Pixel Pitch	[mm]	0.213x 0.213
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN, Normally White
Nominal Input Voltage VDD	[Volt]	3.3 typ.
Typical Power Consumption	[Watt]	5.8 typ.
Weight	[Grams]	260 ±10 (typ.)
Physical Size	[mm]	203.0(W) x 142.5(H) x 8.0(D) (typ.)
Electrical Interface		1 channel LVDS
Surface Treatment		Anti-glare, Hardness 3H
Support Color		262K(6-bit) / 16.2M(8-bit)
Temperature Range Operating Storage (Non-Operating)	 [°C] [°C]	 -30 to +85 (+85°C as panel surface temp.) -30 to +85
RoHS Compliance		RoHS Compliance

2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25℃ (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.	Note
White Luminance	[cd/m2]	IRCFL= 6mA (center point)	350	450	-	1
Uniformity	%	9 Points	70	75	-	1, 2, 3
Contrast Ratio			400	600	-	4
Response Time	[msec]	Rising	-	10		5
	[msec]	Falling	-	25		
	[msec]	Raising + Falling	-	35		
Viewing Angle	[degree]	Horizontal (Right) CR = 10	-	75	-	6
	[degree]	(Left)	-	75	-	
	[degree]	Vertical (Upper) CR = 10	-	75	-	
	[degree]	(Lower)	-	75	-	
Color / Chromaticity Coordinates (CIE 1931)		Red x	TBD	TBD	TBD	
		Red y	TBD	TBD	TBD	
		Green x	TBD	TBD	TBD	
		Green y	TBD	TBD	TBD	
		Blue x	TBD	TBD	TBD	
		Blue y	TBD	TBD	TBD	
		White x	0.28	0.31	0.34	
		White y	0.3	0.33	0.36	
Color Gamut	%			45	-	

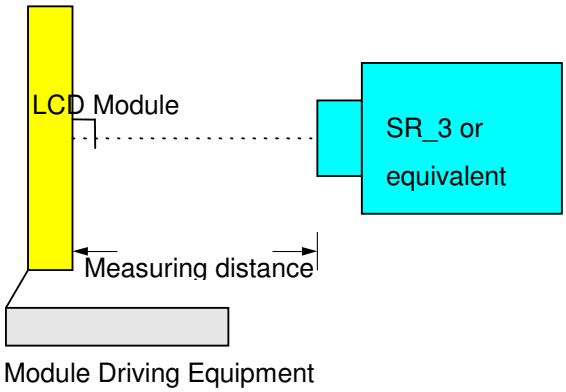
Note 1: Measurement method

Equipment Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR_3 or equivalent)

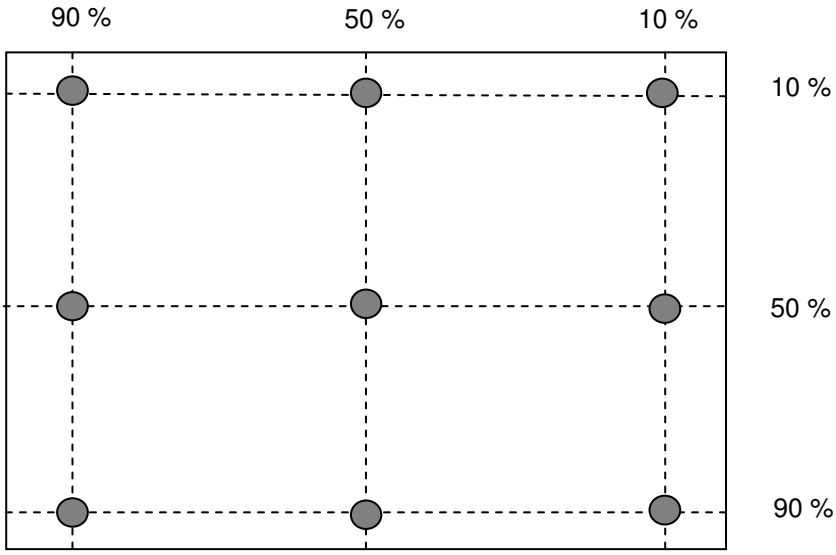
Aperture 1° with 50cm viewing distance

Test Point Center

Environment *< 1 lux*



Note 2: Definition of 9 points position (Display active area : 170.4(H) x 127.8(V))



Note 3: The luminance uniformity of 9 points is defined by dividing the minimum luminance values by the maximum test point luminance

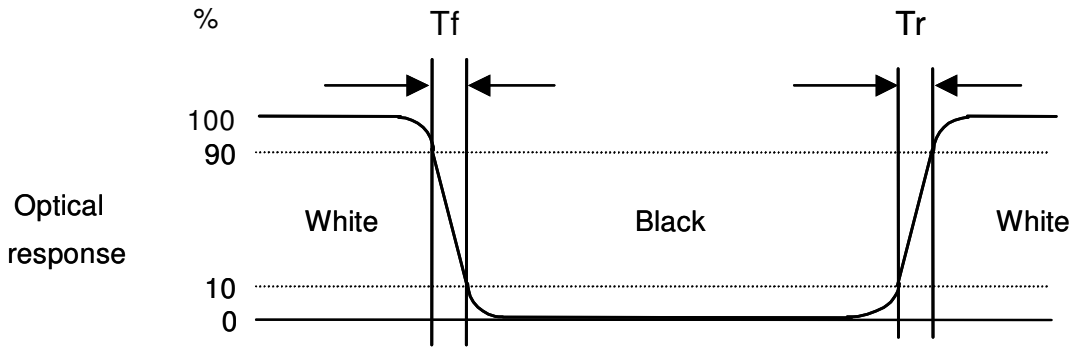
$$\delta_{W9} = \frac{\text{Minimum Brightness of nine points}}{\text{Maximum Brightness of nine points}}$$

Note 4： Definition of contrast ratio (CR):

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the “White” state}}{\text{Brightness on the “Black” state}}$$

Note 5: Definition of response time:

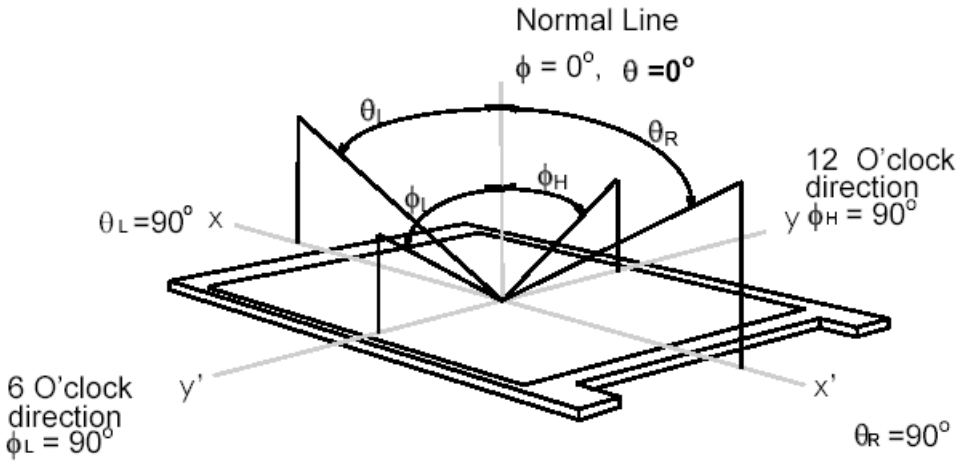
The output signals of photo detector are measured when the input signals are changed from “White” to “Black” (falling time) and from “Black” to “White” (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



Note 6: Definition of viewing angle

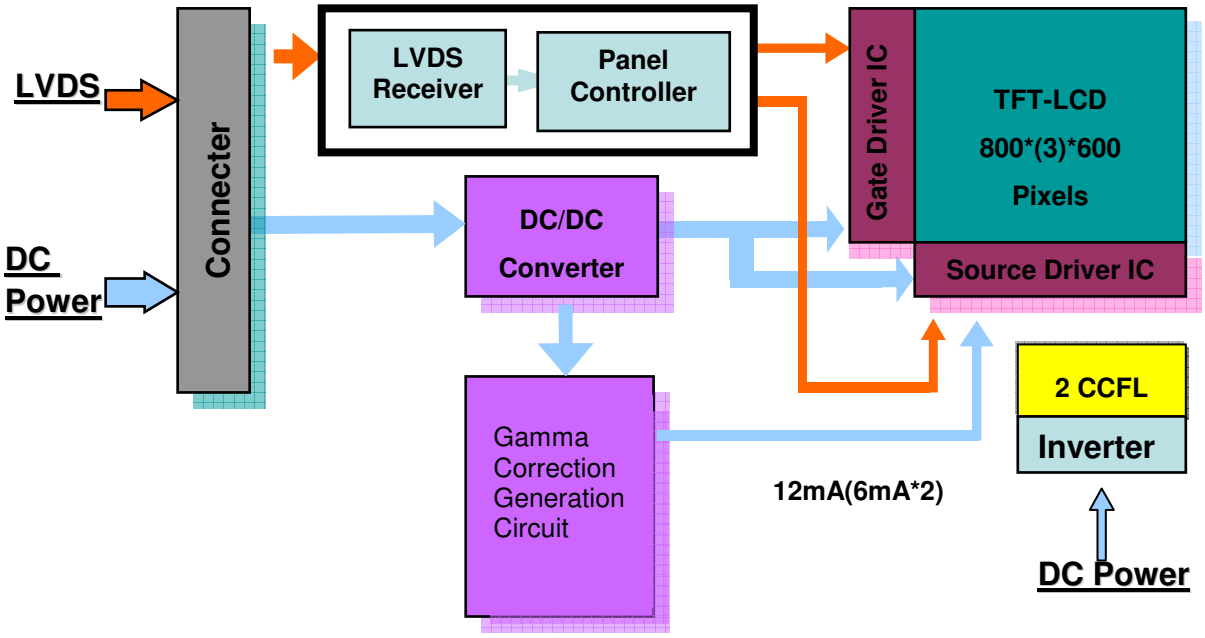
Viewing angle is the measurement of contrast ratio ≧10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: 90° (θ)

horizontal left and right, and 90° (Φ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.



3. Functional Block Diagram

The following diagram shows the functional block of the 8.4 inch color TFT/LCD module:



4. Absolute Maximum Ratings

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vin	-0.3	+3.6	[Volt]	
Input Voltage of Signal	Vin	-0.3	VDD+0.3	[Volt]	
CCFL Current	ICFL	4	7	[mA] rms	
CCFL Ignition Voltage	Vs	-	650	Vrms	25°C
			850		0°C

4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-30	+85	[°C]
Operation Humidity	HOP	8	95	[%RH]
Storage Temperature	TST	-30	+85	[°C]
Storage Humidity	HST	5	95	[%RH]

Note: Maximum Wet-Bulb should be 39℃ and no condensation.

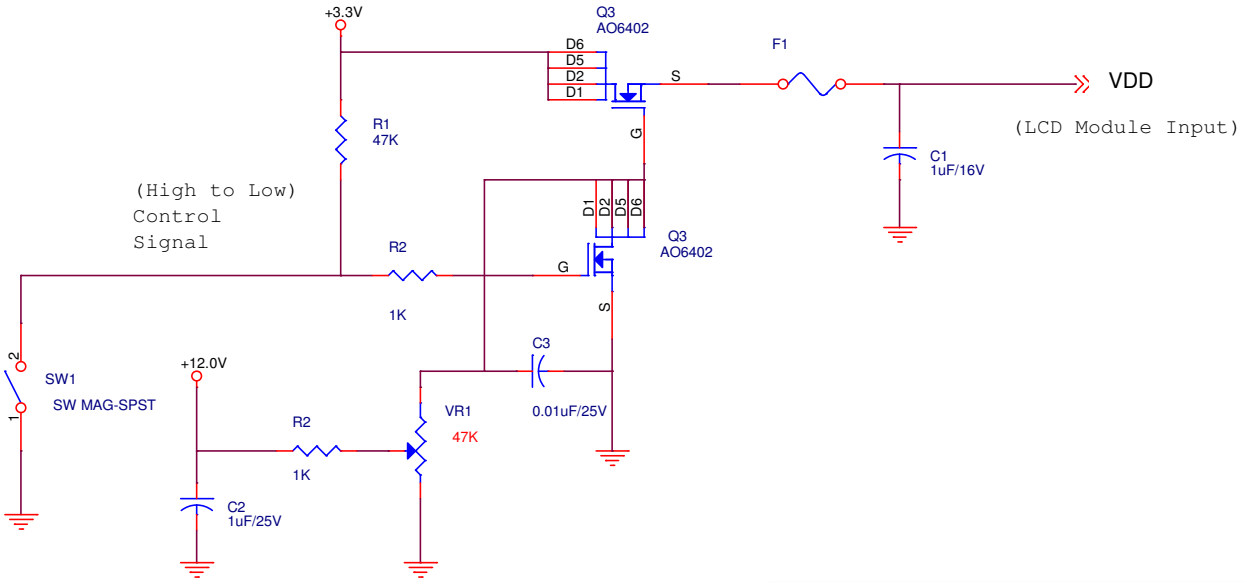
5. Electrical Characteristics

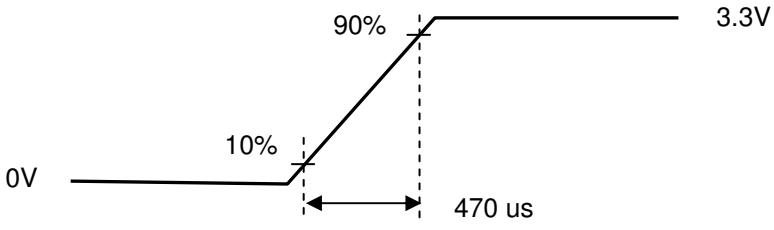
5.1 TFT LCD Module

5.1.1 Power Specification

Symbol	Parameter	Min	Typ	Max	Units	Remark
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	±10%
IDD	VDD Current	-	300	330	[mA]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)
Irush	LCD Inrush Current	-	-	3	[A]	Note 1
PDD	VDD Power	-	1	1.2	[Watt]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)

Note 1: Measurement condition:





VDD rising time

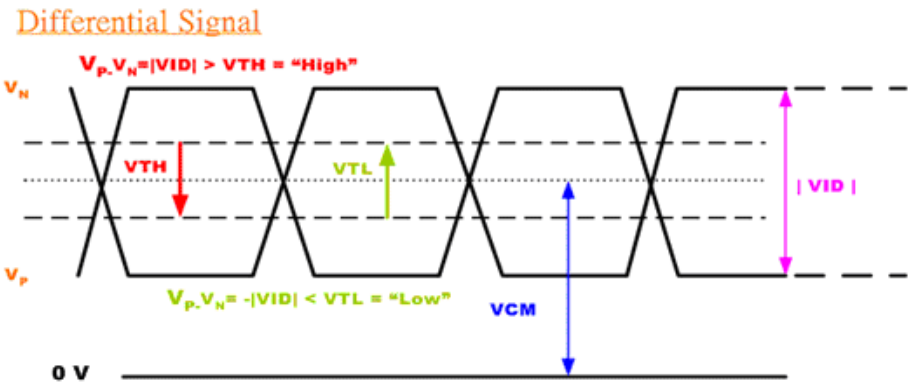
64 Gray pattern

5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Symbol	Item	Min.	Typ.	Max.	Unit	Remark
VTH	Differential Input High Threshold	-	-	100	[mV]	VCM=1.2V
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VCM=1.2V
| VID |	Input Differential Voltage	100	400	600	[mV]	
VICM	Differential Input Common Mode Voltage	1.1		1.6	[V]	VTH/VTL=±100mV

Note: LVDS Signal Waveform.



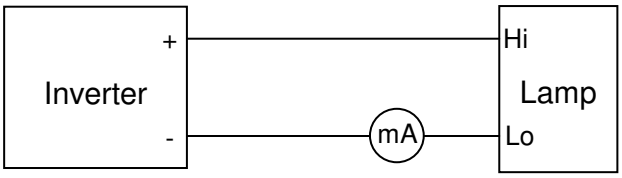
5.2 Backlight Unit

5.2.1 Parameter guideline for CCFL

Following characteristics are measured under a stable condition using an inverter at 25℃ (Room Temperature):

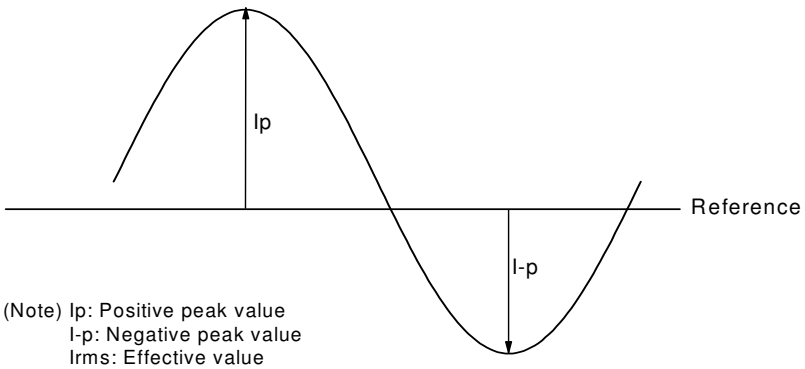
Symbol	Parameter	Min.	Typ.	Max.	Unit	Remark
IRCFL	CCFL operation range	4	6	7	[mA] rms	(Ta=25°C) Note 1
FCFL	CCFL Frequency	40	55	60	[KHz]	(Ta=25°C) Note 2
ViCFL (0°C) (reference)	CCFL Ignition Voltage	-	-	850	[Volt] rms	(Ta= 0°C)
ViCFL (25°C) (reference)	CCFL Ignition Voltage	-	-	650	[Volt] rms	(Ta=25°C)
VCFL	CCFL Discharge Voltage	387	430	473	[Volt] rms	(Ta=25°C) Note 3 IRCFL=6.0mA VCFL = Typ±10%
PCFL	CCFL Power consumption (inverter excluded)	-	5.16	-	[Watt]	(Ta=25°C) Note 3 IRCFL=6.0mA
Lamp Life		50,000	-		Hrs	(Ta=25°C) Note 4 IRCFL = 6.0mA

Note 1: IRCFL is defined as the return current of an inverter. (In Figure. 1)



(Figure. 1: Measurement of return current)

A stable IRCFL is a current without flicker or biasing waveform provided by inverter that ensures the backlight perform to its specification. The ideal sine waveform should be symmetric in positive and negative polarities and the asymmetry rate of the inverter waveform should be below 10%.



(Note) Ip: Positive peak value
I-p: Negative peak value
Irms: Effective value

DC Bias = (| Ip – I-p| / Irms) x 100 % <10%

Crest Factor = Ip or (I-p) / Irms should have the range within 1.414 ± 10%

It is recommended to use the inverter with detection circuit (ie: balance and protection circuit) to avoid overvoltage, overcurrent, or mismatching waveform.

Note 2: CCFL frequency should be carefully determined to avoid interference between inverter and TFT LCD. Higher frequency will induce higher leakage current and further impact lamp life.

Note 3: Calculator value for reference (IRCFLxVCFLx2=PCFL).

Note 4: The definition of lamp life means when any of following conditions happen:

a) Luminance falls to 50% or less of the initial value.
b) Normal lighting is no more available (flickeRxINg, pink lighting, no lighting, etc.)
c) Lamp voltage or lighting start voltage exceeds the specified value.

Lamp life time shortens according to

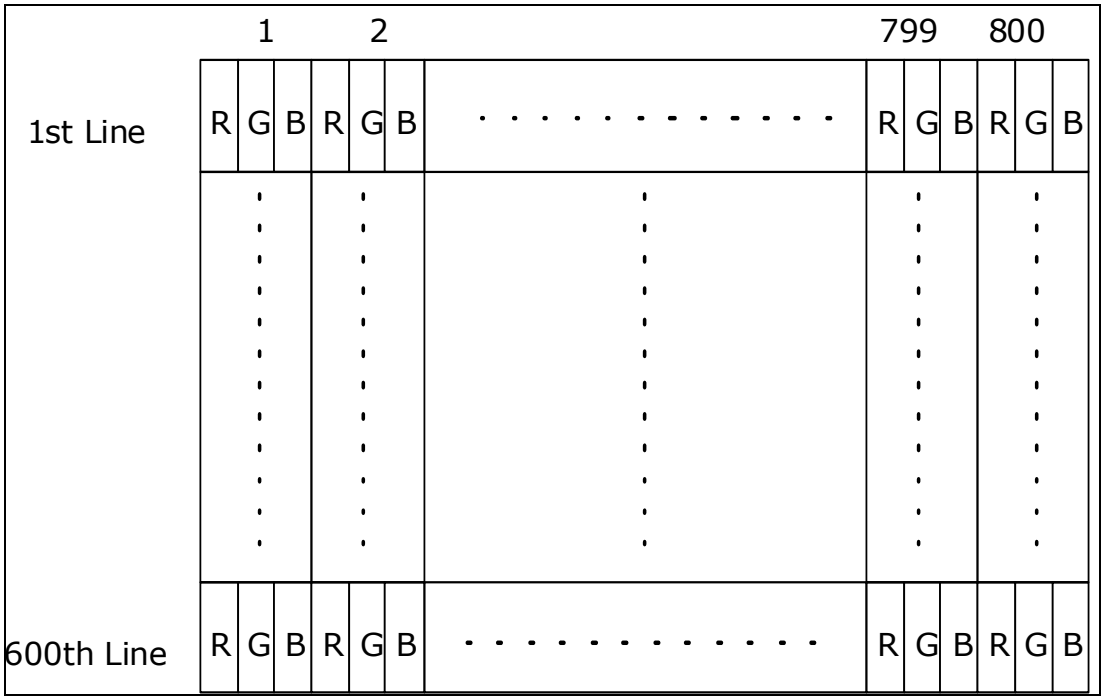
a) Placing methodology: mercury is unevenly distributed in portrait mounting
b) Environmental condition: low temperature reduces the presence of mercury vapor, which results in approximately lamp life of 1,000 hours
c) CCFL surface temperature: Presence of gradient in lamp surface temperature causes uneven mercury migration
d) Inverter design: its resonance capacitor should be fine-tuned with the impedance of CCFL
e) Over driving current (> 6.5 mA) shortens lamp life time dramatically.

Note 5: The display is with dual lamp design, and the CCFL current in above table refers to each lamp

6. Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.



6.2 Scanning Direction

The following figures show the image seen from the front view. The arrow indicates the direction of scan.

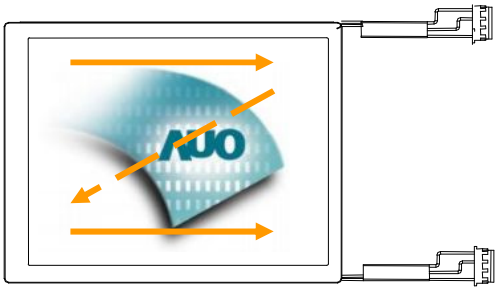


Fig. 1

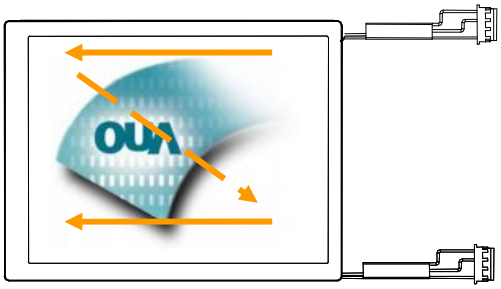


Fig. 2

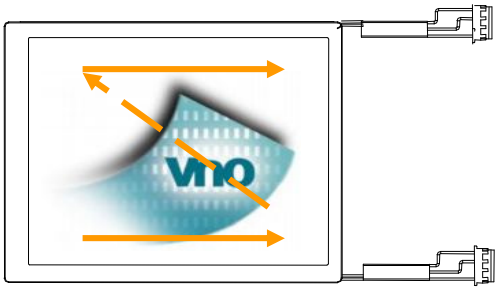


Fig. 3

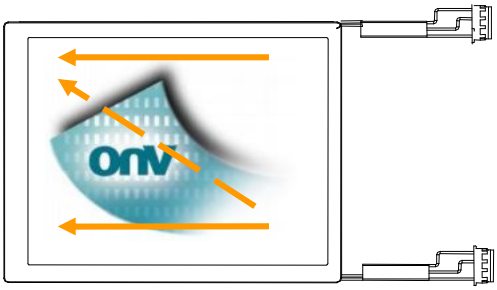


Fig. 4

Fig. 1 Normal scan (Pin3, UD = Low or NC ; Pin4, RL = Low or NC)

Fig. 2 Reverse scan (Pin3, UD = Low or NC ; Pin4, RL = High)

Fig. 3 Reverse scan (Pin3, UD = High ; Pin4, RL = Low or NC)

Fig. 4 Reverse scan (Pin3, UD = High ; Pin4, RL = High)

6.3 Signal Description

LVDS is a differential signal technology for LCD interface and high speed data transfer device. The connector pin definition is as below.

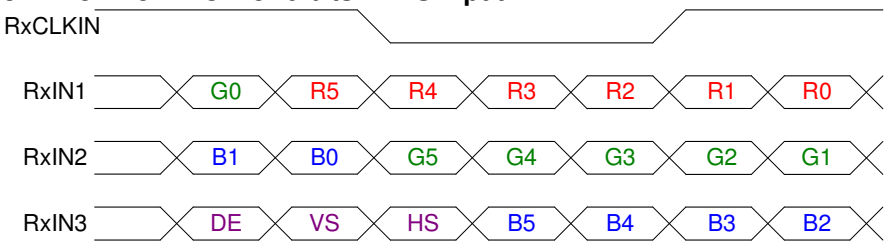
Pin No.	Symbol	Description
1	VDD	Power Supply, 3.3V (typical)
2	VDD	Power Supply, 3.3V (typical)
3	UD	Vertical Reverse Scan Control, Low or NC → Normal Mode. High → Vertical Reverse Scan.Note
4	LR	Vertical Reverse Scan Control, Low or NC → Normal Mode. High → Vertical Reverse Scan.Note
5	RxIN0-	LVDS differential data input Pair 0
6	RxIN0+	
7	GND	Ground
8	RxIN1-	LVDS differential data input Pair 1
9	RxIN1+	
10	GND	Ground
11	RxIN2-	LVDS differential data input Pair 2
12	RxIN2+	
13	GND	Ground
14	RXCLKIN-	LVDS differential Clock input Pair
15	RXCLKIN+	
16	GND	Ground
17	SEL 68	LVDS 6/8 bit select function control, Low or NC → 6 Bit Input Mode. High → 8 Bit Input Mode. Note
18	NC	NC
19	RxIN3-	LVDS differential data input Pair 3. Must be tied to Ground in 6 bit input mode.
20	RxIN3+	

Note : “Low” stands for 0V. “High” stands for 3.3V. “NC” stands for “No Connected.”

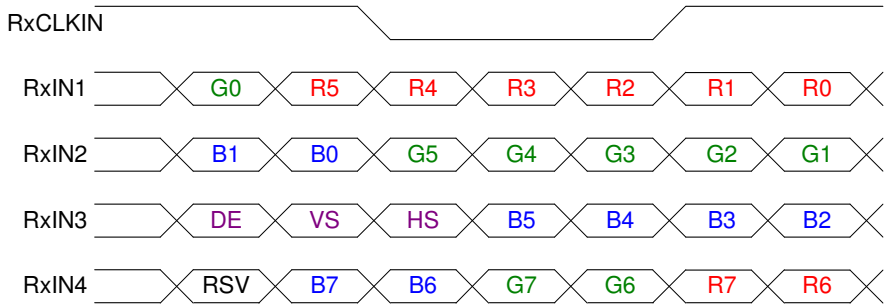
6.4 The Input Data Format

6.4.1 SEL68

SEL68 = ”Low” or “NC” for 6 bits LVDS Input



SEL68 = “High” for 8 bits LVDS Input



Note1: Please follow PSWG.
Note2: R/G/B data 7:MSB, R/G/B data 0:LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 (MSB) Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel’s brightness data consists of these 8 bits pixel data.
G7 G6 G5 G4 G3 G2 G1 G0	Green Data 7 (MSB) GreenData 6 GreenData 5 GreenData 4 GreenData 3 GreenData 2 GreenData 1 GreenData 0 (LSB)	Green-pixel Data Each green pixel’s brightness data consists of these 8 bits pixel data.
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 (MSB) Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel’s brightness data consists of these 8 bits pixel data.
RXCLKIN+ RXCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.

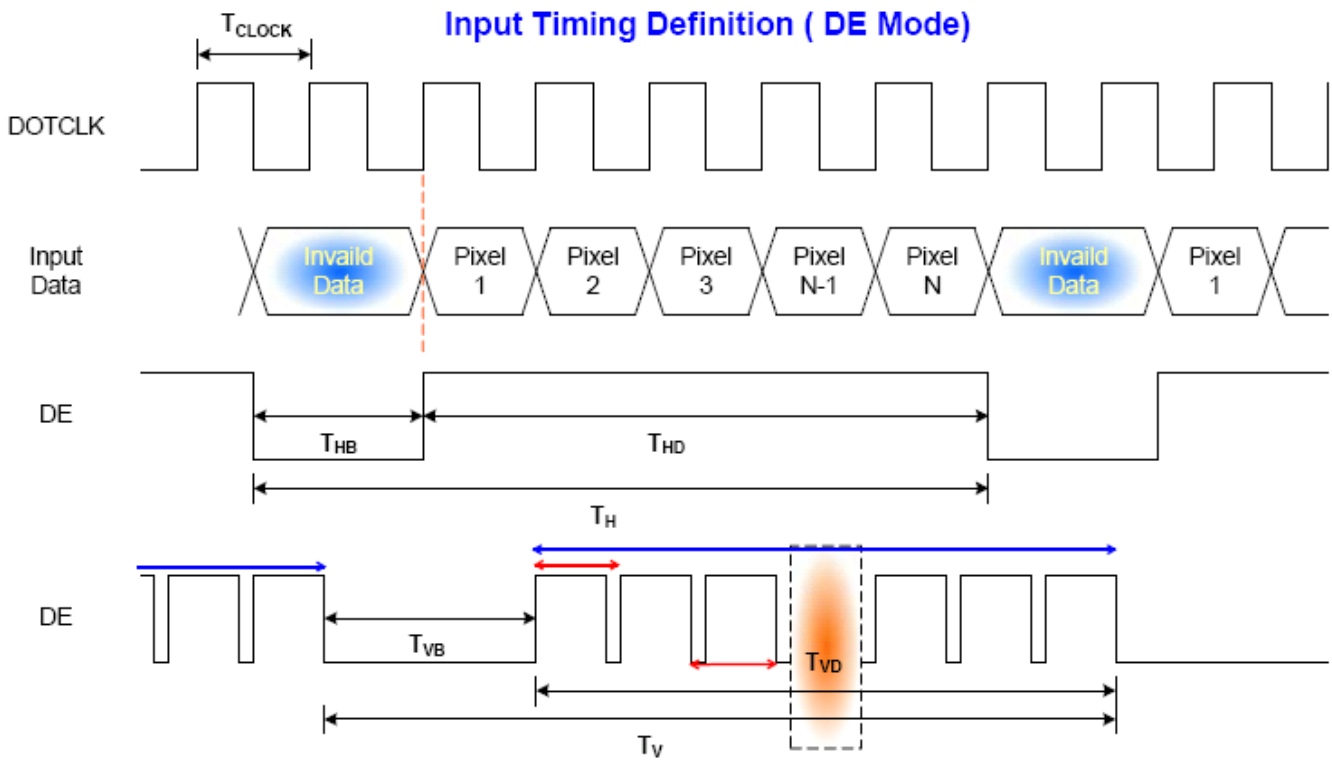
6.5 Interface Timing

6.5.1 Timing Characteristics

DE mode only

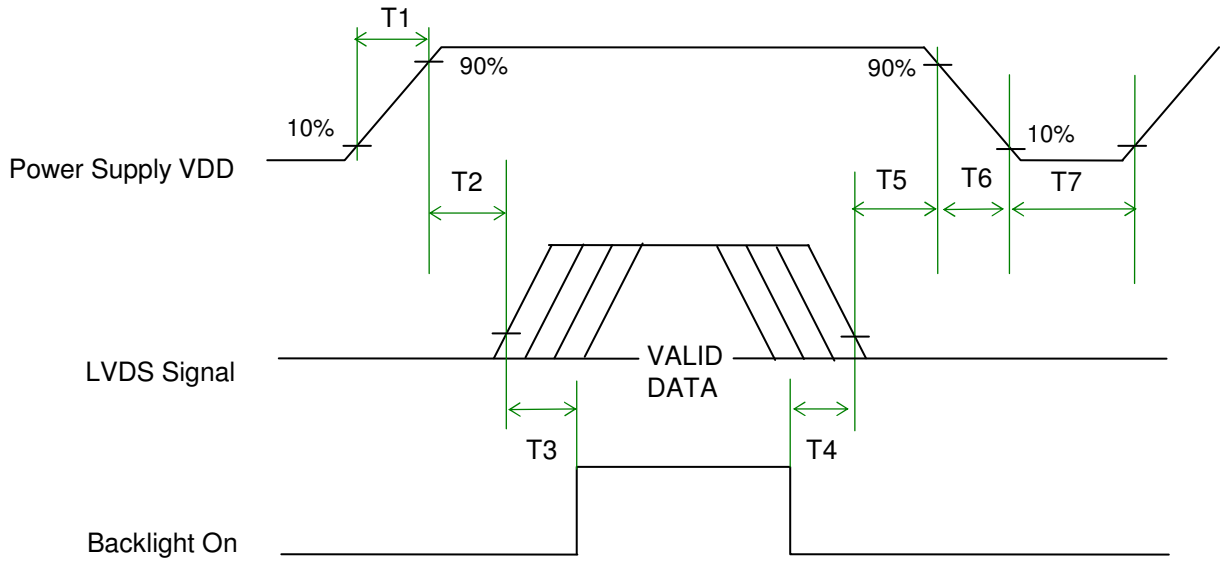
Parameter		Symbol	Min.	Typ.	Max.	Unit	Condition
Clock frequency		1/ T_{Clock}	33.6	39.8	48.3	MHz	
Vertical Section	Period	T_V	608	628	650	T_H	
	Active	T_{VD}	600	600	600		
	Blanking	T_{VB}	8	28	50		
Horizontal Section	Period	T_H	920	1056	1240	T_{Clock}	
	Active	T_{HD}	800	800	800		
	Blanking	T_{HB}	120	256	440		

6.5.2 Input Timing Diagram



6.6 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	-	10	ms
T2	0	40	50	ms
T3	200	-	-	ms
T4	200	-	-	ms
T5	0	16	50	ms
T6	0	-	10	ms
T7	1000	-	-	ms

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

7. Connector & Pin Assignment

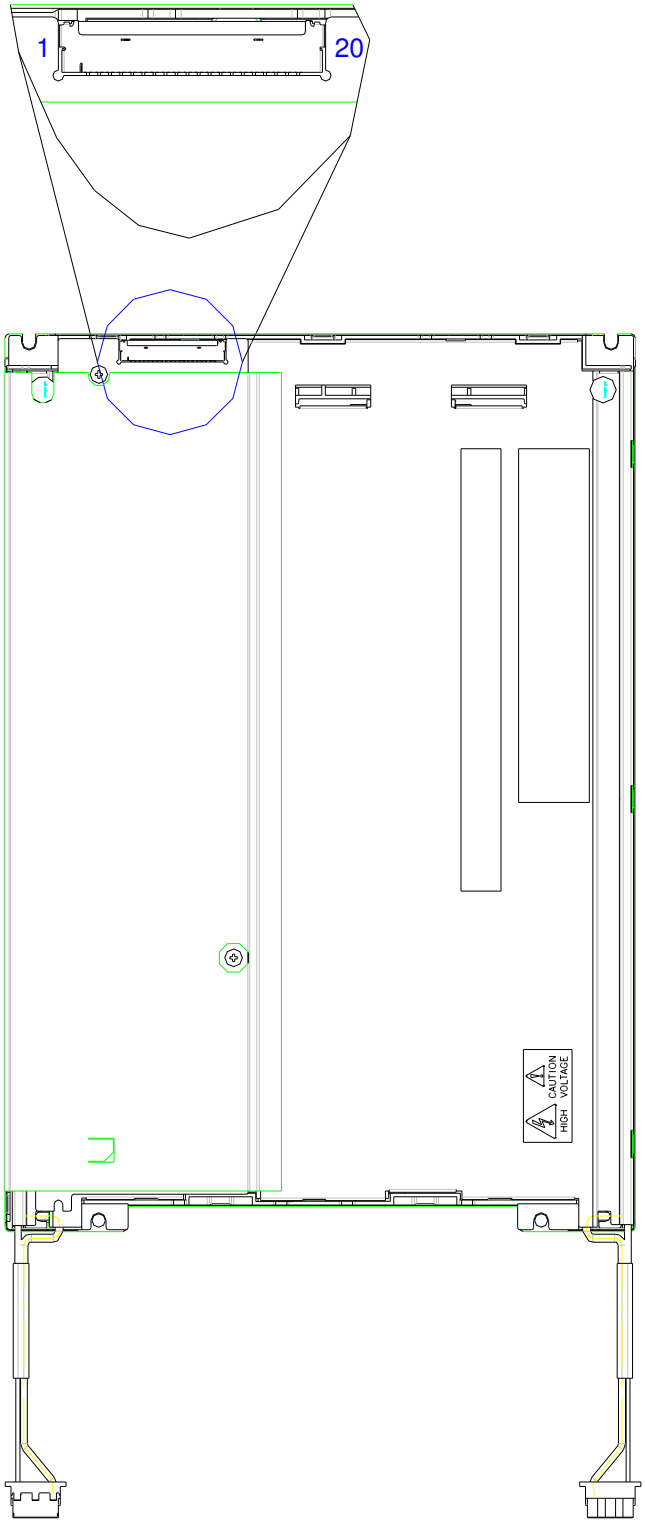
Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module: LVDS Connector

Connector Name / Designation	**Signal Connector**
Manufacturer	STM
Connector Model Number	MSB24013P20 or compatible.
Adaptable Plug	P24013P20

Pin No.	Signal Name	Pin No.	Signal Name
1	**VDD**	**2**	**VDD**
3	**UD**	**4**	**LR**
5	**RxIN0-**	**6**	**RxIN0+**
7	**GND**	**8**	**RxIN1-**
9	**RxIN1+**	**10**	**GND**
11	**RxIN2-**	**12**	**RxIN2+**
13	**GND**	**14**	**CKIN-**
15	**CKIN+**	**16**	**GND**
17	**SEL 68**	**18**	**NC**
19	**RxIN3+**	**20**	**RxIN3-**

7.1.1 Connector Illustration



7.2 Backlight Unit: Lamp Connector

Connector Name / Designation	**Lamp Connector**
Manufacturer	HIROSE
Connector Model Number	JST-BHR-03VS-1
Mating Model Number	JST-BHMR-03V

7.3 Lamp Connector Pin Assignment

Pin#	**Symbol**	**Cable color**	**Signal Name**
1	1	Red	High Voltage
2	2	NC	No Connection
3	3	White	Low Voltage

- Cable length: 57 ± 5 m m
- Connector-output position: right side (front view)
- Lamp assembly design shall be easy for replacement and repair

8. Reliability Test Criteria

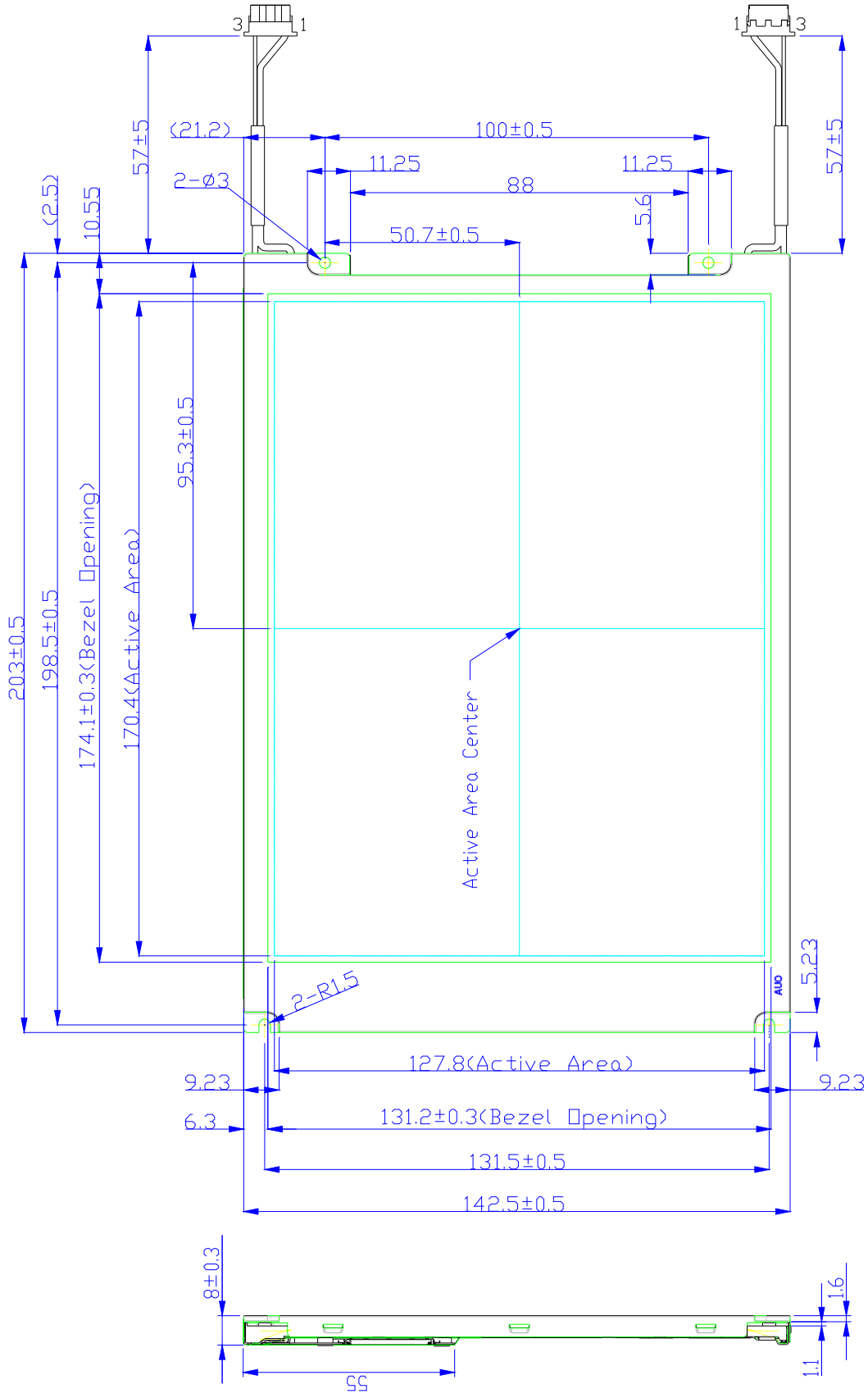
Items	Required Condition	Note
Temperature Humidity Bias	40℃/90%,500 hours	
High Temperature Operation	85℃,300 hours	
Low Temperature Operation	-30℃,300 hours	
Hot Storage	85℃,300 hours	
Cold Storage	-30℃,300 hours	
Thermal Shock Test	-20℃/30 min ,60℃/30 min ,100cycles	
Shock Test (Non-Operating)	50G,20ms,Half-sine wave,(±X, ±Y, ±Z)	
Vibration Test (Non-Operating)	1.5G, (10~200Hz, P-P) 30 mins/axis (X, Y, Z)	
On/off test	On/10 sec, Off/10 sec, 30,000 cycles	
ESD	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point Air Discharge: ± 15KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point	Note 1

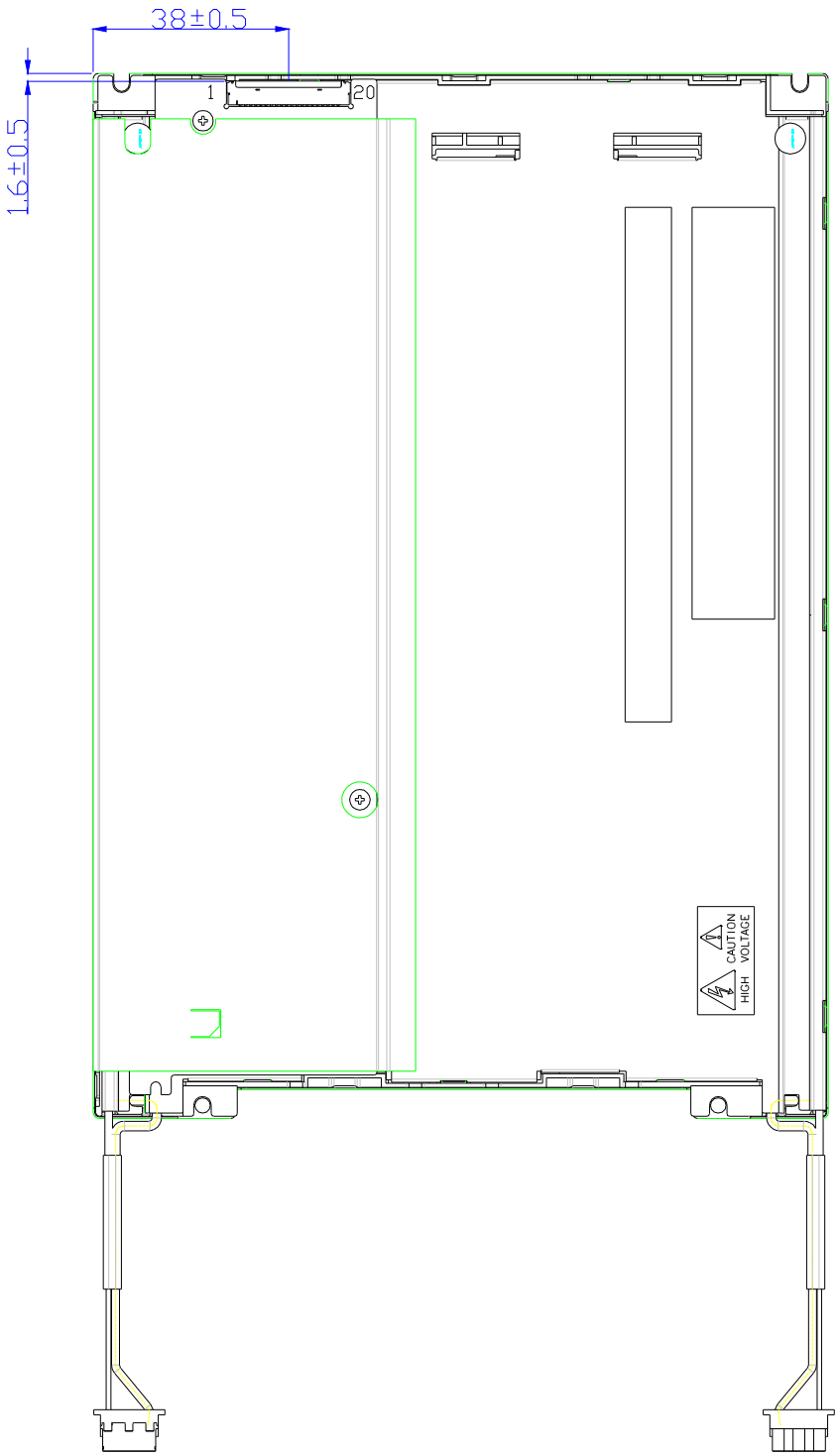
Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost

. Self-recoverable. No hardware failures.

9. Mechanical Characteristics

9.1 LCM Front View and Rear View




38±0.5
1.6±0.5
1
20
CAUTION
HIGH VOLTAGE

10. Label and Packaging

10.1 Shipping Label (on the rear side of TFT-LCD display)

TBD

10.2 Carton Package

TBD

11 Safety

11.1 Sharp Edge Requirements

There will be no sharp edges or comers on the display assembly that could cause injury.

11.2 Materials

11.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible AUO toxicologist.

11.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process.

The pRxINted circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be pRxINted on the pRxINted circuit board.

11.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

11.4 National Test Lab Requirement

The display module will satisfy all requirements for compliance to:

UL 1950, First Edition	U.S.A. Information Technology Equipment